EAST Search History

EAST Search History (Prior Art)

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(data and line and peripheral and capacitor and wire and film and aluminum and substrate and terminal).clm.	US-PCIPUB; USPAT; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/09/23 16:42
L2	O	0 (data and line and peripheral and capacitor and wire and film and aluminum and substrate and terminal and region and exterior).clm.		OR .	ON	2009/09/23 16:43
L3	1632	(345/92).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		OFF	2009/09/23 16:43
L4	4444	(345/204).CCLS.	US-PCPUB; USPAT; USOCR; EPO; JPO; DEFWENT; IBM_TDB		OFF	2009/09/23 16:43
L5	15935	capacitor near5 wire	US-PGPUB; USPAT; USOOR; EPO; JPO; DEFWENT; IBM_TDB		ON	2009/09/23 16:43
L6	26695	tft near10 LCD	US-PGPUB; USPAT; USOCR; EPO; JPO; DEFMENT; IBM_TDB		ON	2009/09/23 16:43
L7	35086	(exterior or external) near10 circuit\$1 near10 connection\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/23 16:43

L8	998	lattice same capacitor\$1 near10 (line\$1 or lead\$1 or wirless or electrode\$1 or connection\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/23 16:44
L9	190559	(capacitor or capacitance) near20 (wire\$1 or line\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/23 16:44
L10	12753	lattice near5 pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/23 16:44
L11	49	L9 same L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/23 16:44
L12	386	(349/111).COLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/23 16:44
L13	2468480	capacitor\$1 near "10" wire\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/09/23 16:44
L14	374081	"same" near5 (substrate\$1 or film\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/23 16:44
L15	54	L13 same L7 same L14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/09/23 16:44

EAST Search History (Interference)

Ref# Hi	ts Search Query	DBs	Default Operator Plurals Time Stamp

L16	0	(data and line and peripheral and	USPAT; UPAD	OR	ON	2009/09/23 16:46
				-		
		capacitor and wire				
		and film and				
		aluminum and				
		substrate and				
		terminal and				
		region and				
		exterior).clm.				

9/23/2009 4:46:29 PM C:\ Documents and Settings\ cma\ My Documents\ EAST\ Workspaces\ 1932.wsp